



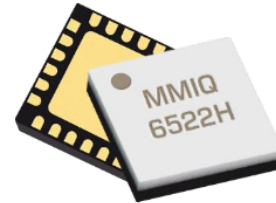
Passive GaAs MMIC IQ Mixer

MMIQ-0416HSM

1. Device Overview

1.1 General Description

MMIQ-0416HSM is a high linearity, passive GaAs MMIC IQ mixer. This is an ultra-broadband mixer spanning 4 to 16 GHz on the RF and LO ports with an IF from DC to 6 GHz. Up to 40 dB of image rejection is available due to the excellent phase and amplitude balance of its on-chip LO quadrature hybrid. The MMIQ 0416HSM is available in a 4x4 mm QFN package. Evaluation boards are available. For a list of recommended LO driver amps for all mixers and IQ mixers, see [here](#).



QFN

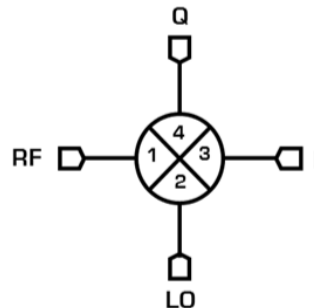
1.2 Features

| Parameter | Typical | Unit |
|-----------------------|---------|------|
| RF/LO Frequency Range | 4 - 16 | GHz |
| IF Frequency Range | DC – 6 | GHz |
| I+Q Conversion Loss | 8.5 | dB |
| Image Rejection | 31 | dB |
| LO-RF Isolation | 51 | dB |

1.3 Applications

- Single Side Band & Image Rejection Mixing
- IQ Modulation/Demodulation
- Vector Amplitude Modulation
- Band Shifting

1.4 Functional Block Diagram



1.5 Part Ordering Options¹

| Part Number | Description | Package | Green Status | Product Lifecycle | Export Classification |
|-----------------|---|---------|--------------|-------------------|-----------------------|
| MMIQ-0416HSM-2 | 4x4 mm ² QFN | SM | RoHS | Active | EAR99 |
| EVAL-MMIQ-0416H | Connectorized module, QFN reflowed onto PCB | EVAL | Non-RoHS | Active | EAR99 |

¹ Refer to our [website](#) for a list of definitions for terminology presented in this table.



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Revision History

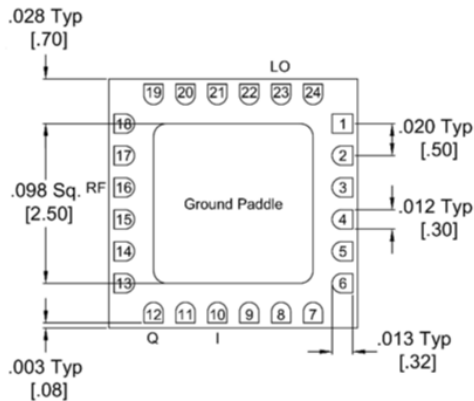
| Revision Code | Revision Date | Comment |
|---------------|---------------|---------------------------------|
| - | March 2019 | Datasheet Initial Release |
| A | August 2019 | Changed I/Q Max Current Rating |
| B | October 2019 | Updated Max Power Handling Spec |



2. Port Configurations and Functions

2.1 Port Diagram

A bottom-up view of the MMIQ-0416H's SM package outline drawing is shown below. The mixer may be operated as either a downconverter or an upconverter. Use of the RF or IF as the input or output port will depend on the application.



2.2 Port Functions

| Port | Function | Description | Equivalent Circuit for Package |
|--------|------------------|--|--------------------------------|
| Pin 16 | RF Input/Output | Pin 16 is DC short and AC matched to 50Ω over the specified RF frequency range. | P16 |
| Pin 23 | LO Input | Pin 23 is DC open and AC matched to 50Ω over the specified LO frequency range. | P23 |
| Pin 10 | I Input / Output | Pin 10 is diode coupled and AC matched to 50Ω over the specified I port frequency range. | P10 |
| Pin 12 | Q Input / Output | Pin 12 is diode coupled and AC matched to 50Ω over the specified Q port frequency range. | P12 |



3. Specifications

3.1 Absolute Maximum Ratings

The Absolute Maximum Ratings indicate limits beyond which damage may occur to the device. If these limits are exceeded, the device may be inoperable or have a reduced lifetime.

| Parameter | Maximum Rating | Units |
|-----------------------------|----------------|-------|
| Pin 10 DC Current | 30 | mA |
| Pin 12 DC Current | 30 | mA |
| Power Handling, at any Port | +26 | dBm |
| Operating Temperature | -55 to +100 | °C |
| Storage Temperature | -65 to +125 | °C |

3.2 Package Information

| Parameter | Details | Rating |
|-----------|--|--------|
| ESD | Human Body Model (HBM), per MIL-STD-750, Method 1020 | 1 A |
| Weight | EVAl Package | 10 g |

3.3 Recommended Operating Conditions

The Recommended Operating Conditions indicate the limits, inside which the device should be operated, to guarantee the performance given in Electrical Specifications. Operating outside these limits may not necessarily cause damage to the device, but the performance may degrade outside the limits of the electrical specifications. For limits, above which damage may occur, see Absolute Maximum Ratings.

| | Min | Nominal | Max | Units |
|--------------------------------------|-----|---------|------|-------|
| T _A , Ambient Temperature | -55 | +25 | +100 | °C |
| LO Input Power | +13 | +20 | +22 | dBm |
| RF/IF input power | | | +11 | dBm |

3.4 Sequencing Requirements

There is no requirement to apply power to the ports in a specific order. However, it is recommended to provide a 50Ω termination to each port before applying power. This is a passive diode mixer that requires no DC bias.



3.5 Electrical Specifications

The electrical specifications apply at $T_A=+25^\circ\text{C}$ in a 50Ω system. Typical data shown is for a down conversion application with a +20 dBm sine wave LO input.

Min and Max limits apply only to our connectorized units and are guaranteed at $T_A=+25^\circ\text{C}$. All bare die are 100% DC tested and visually inspected.

| Parameter | Test Conditions | Min | Typical | Max | Units |
|--|--|---------------------------------------|---------|-----|-------|
| RF (Port 1) Frequency Range | | 4 | | 16 | GHz |
| LO (Port 2) Frequency Range | | 4 | | 16 | |
| I (Port 3) Frequency Range | | 0 | | 6 | |
| Q (Port 4) Frequency Range | | 0 | | 6 | |
| Conversion Loss (CL) ² | RF/LO = 4 - 15 GHz I = DC - 0.2 GHz | | 11.7 | 15 | dB |
| | RF/LO = 4 - 15 GHz I = 0.2 - 6 GHz | | 14 | | |
| | RF/LO = 4 - 15 GHz Q = DC - 0.2 GHz | | 11.4 | 15 | |
| | RF/LO = 4 - 15 GHz Q = 0.2 - 6 GHz | | 14 | | |
| Noise Figure (NF) ³ | RF/LO = 4 - 16 GHz I = DC - 0.2 GHz | | 12 | | dB |
| | RF/LO = 4 - 16 GHz Q = DC - 0.2 GHz | | 12 | | |
| Image Rejection (IR) ⁴ | RF/LO = 4 - 16 GHz I+Q = DC - 0.2 GHz | | 31 | | dBc |
| Balance ⁵ | | | 0.5 | | dB |
| Phase Balance | | | 2 | | ° |
| Isolation | LO to RF | RF/LO = 4 - 16 GHz | 51 | | dB |
| | LO to IF | IF/LO = 4 - 16 GHz | 39 | | |
| | RF to IF | RF/IF = 4 - 16 GHz | 31 | | |
| Input IP3 (IIP3) ⁶ | I+Q | RF/LO = 4 - 6 GHz I = DC - 0.2 GHz | 25 | | dBm |
| Input 1 dB Gain Compression Point (P1dB) | I | | 10 | | dBm |
| | Q | | 11 | | |

² Measured as an I/Q down converter (i.e., I and Q powers are not combined)

³ Mixer Noise Figure typically measures within 0.5 dB of conversion loss for IF frequencies greater than 5 MHz.

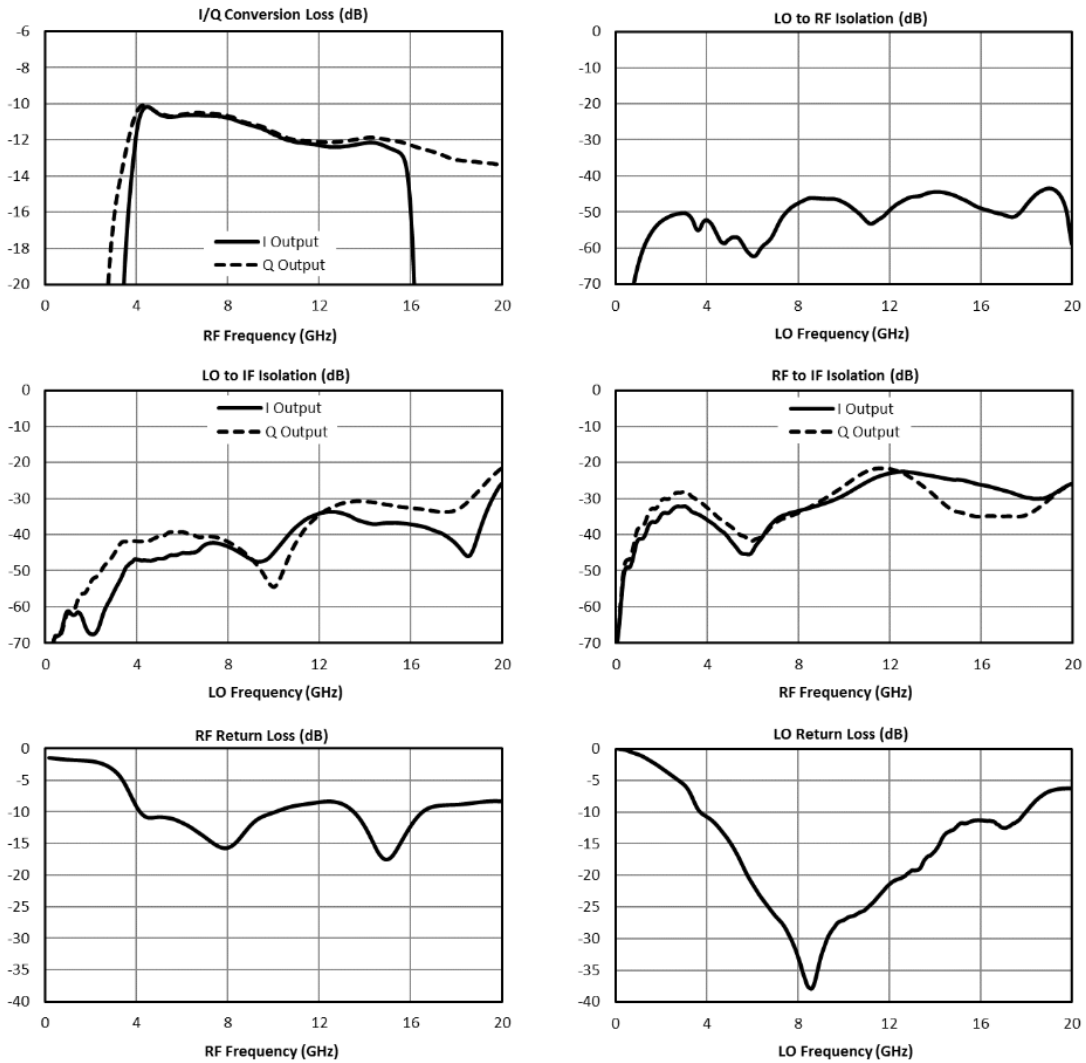
⁴ Image Rejection and Single sideband performance plots are defined by the upper sideband (USB) or lower sideband (LSB) with respect to the LO signal. Plots are defined by which sideband is selected by the external IF quadrature hybrid.

⁵ Amplitude and phase balance measured in a down conversion.

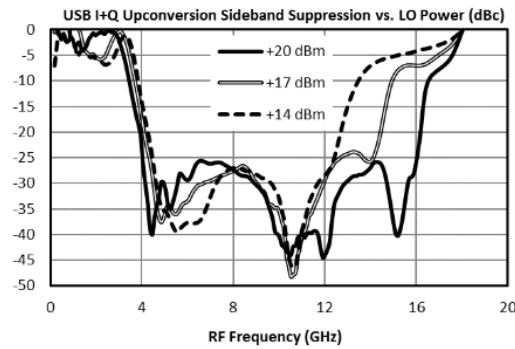
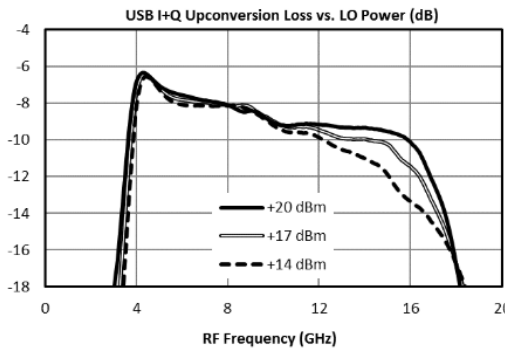
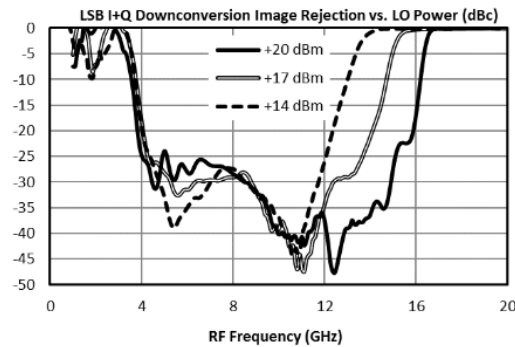
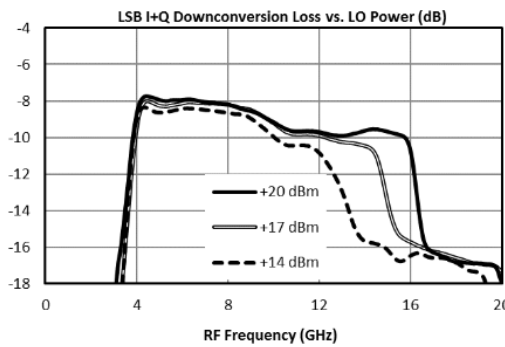
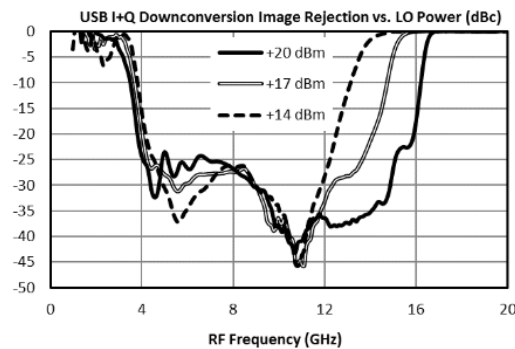
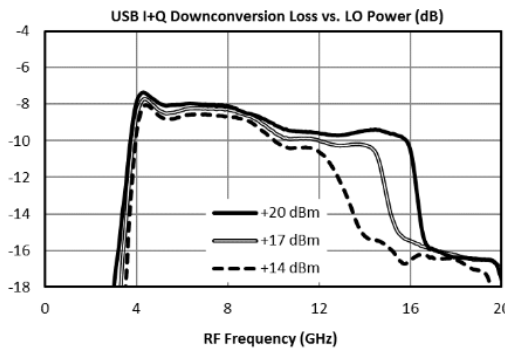
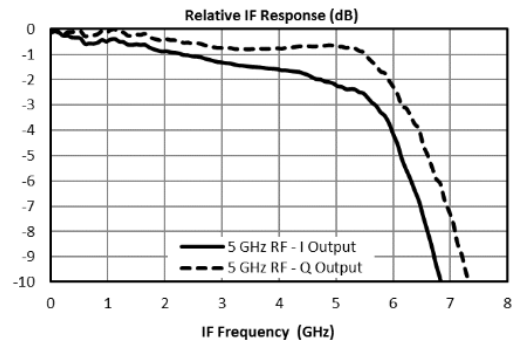
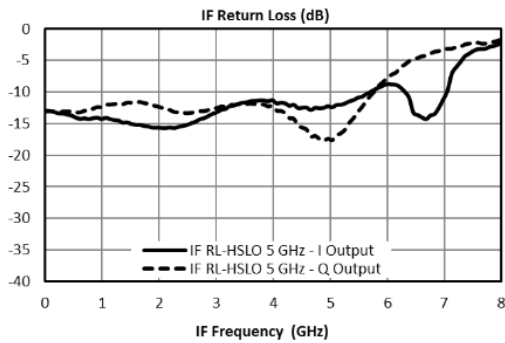
⁶ Typical IIP3 is measured with I and Q ports combined with an external quadrature hybrid coupler in a down conversion.

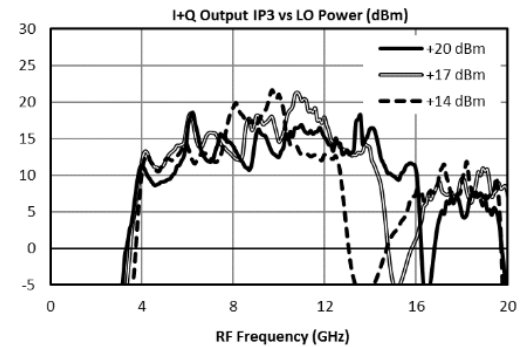
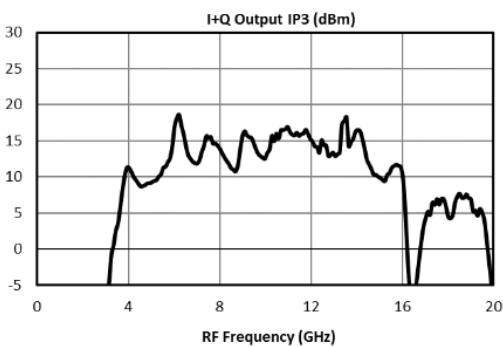
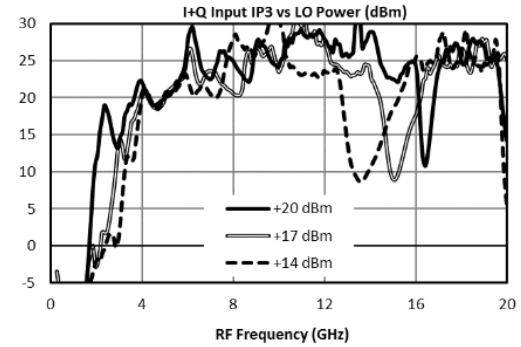
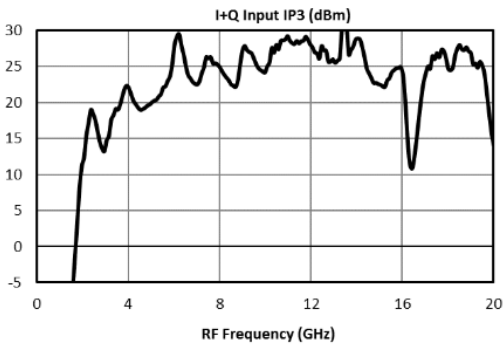
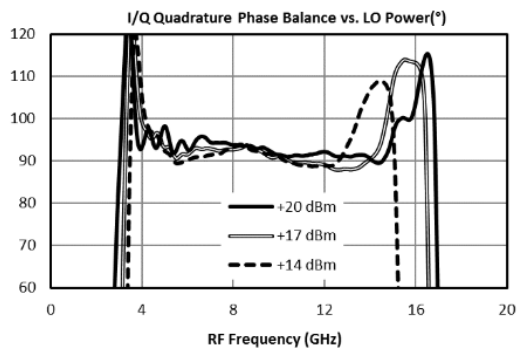
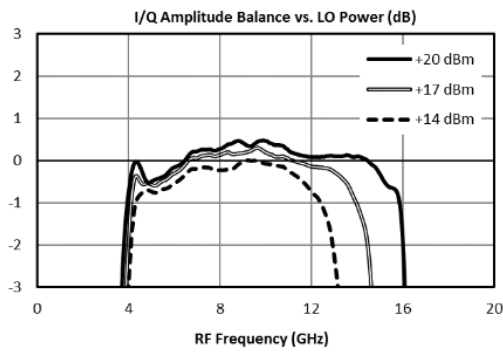
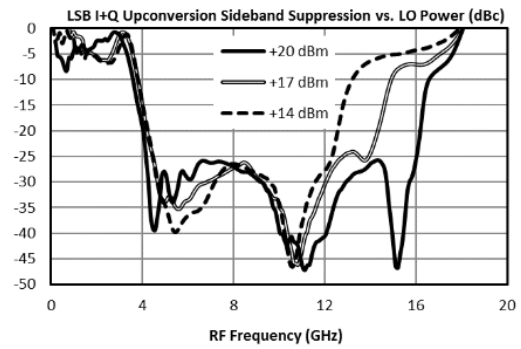
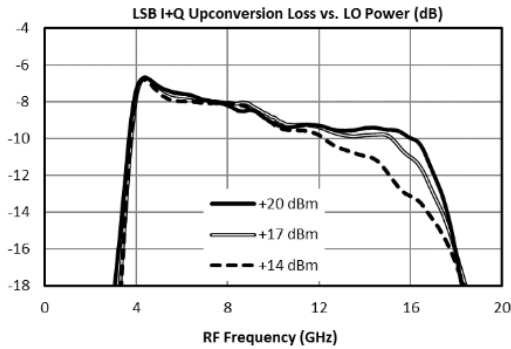


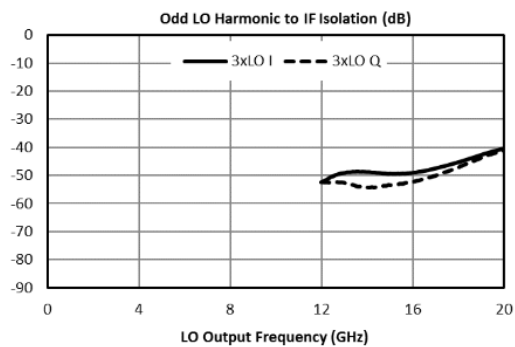
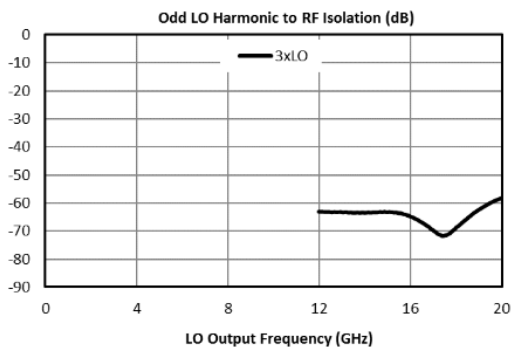
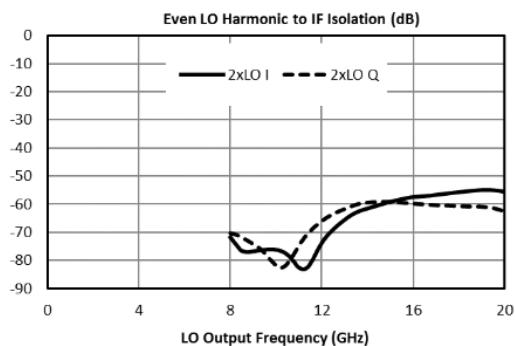
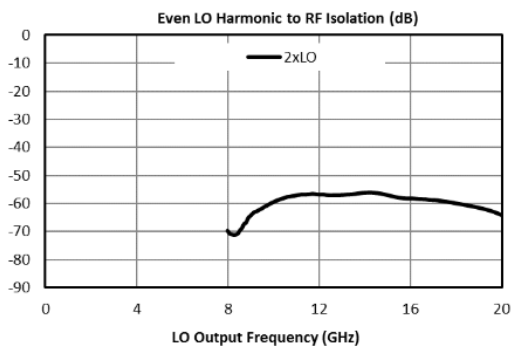
3.6 Typical Performance Plots⁷



⁷ I output means that the IF output signal is measured at the I port of the mixer and the Q port is loaded. Q output means the IF output signal is measured at the Q port of the mixer while the I port is loaded.









3.6.1 Typical Spurious Performance: Down-Conversion

Typical spurious data is provided by selecting RF and LO frequencies ($\pm m \cdot \text{LO} \pm n \cdot \text{RF}$) within the RF/LO bands, to create a spurious output within the IF band. The mixer is swept across the full spurious band and the mean is calculated. The numbers shown in the table below are for a -10 dBm RF input. Spurious suppression is scaled for different RF power levels by $(n-1)$, where "n" is the RF spur order. For example, the 2RF x 2LO spur is 73 dBc for a -10 dBm input, so a -20 dBm RF input creates a spur that is $(2-1) \times (-10 \text{ dB})$ lower, or 83 dBc.

Typical Down-conversion spurious suppression (dBc): I Port (Q Port)

| -10 dBm RF Input | 0xLO | 1xLO | 2xLO | 3xLO | 4xLO | 5xLO |
|------------------|---------|-----------|-----------|-----------|-----------|-----------|
| 0xRF | - | 41 (39) | 70 (68) | 49 (52) | N/A | N/A |
| 1xRF | 31 (31) | Reference | 30 (35) | 16 (11) | N/A | N/A |
| 2xRF | 72 (66) | 53 (59) | 73 (70) | 64 (63) | 64 (66) | 63 (56) |
| 3xRF | 99 (97) | 63 (72) | 93 (95) | 82 (85) | 95 (89) | 85 (82) |
| 4xRF | N/A | N/A | 120 (124) | 116 (121) | 124 (122) | 119 (117) |
| 5xRF | N/A | N/A | 137 (143) | 138 (140) | 138 (141) | 138 (140) |

3.6.2 Typical Spurious Performance: Up-Conversion

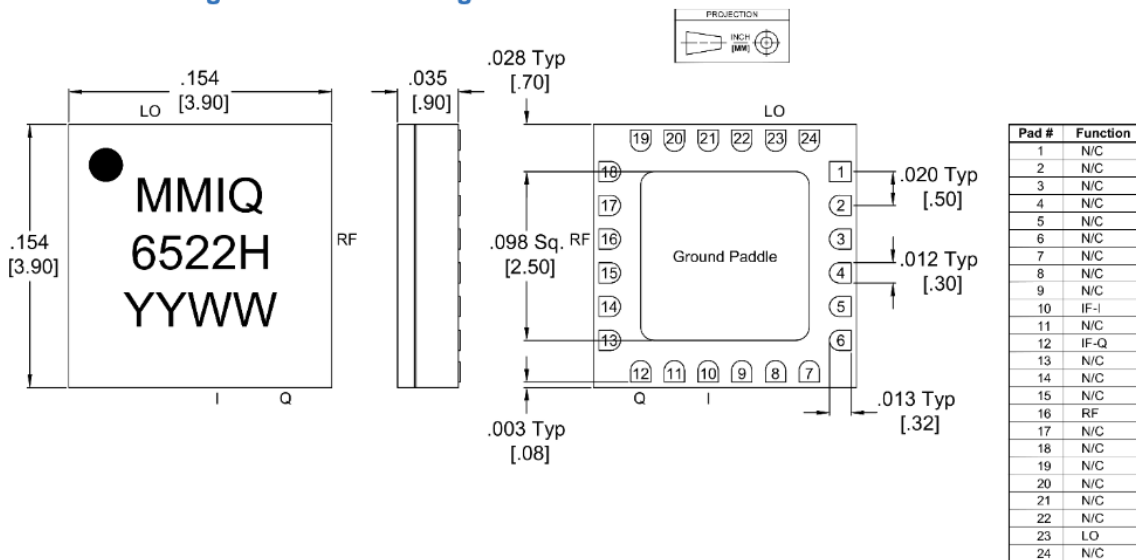
Typical spurious data is taken by mixing an input within the IF band, with LO frequencies ($\pm m \cdot \text{LO} \pm n \cdot \text{IF}$), to create a spurious output within the RF output band. The mixer is swept across the full spurious output band and the mean is calculated. The numbers shown in the table below are for a -10 dBm IF input. Spurious suppression is scaled for different IF input power levels by $(n-1)$, where "n" is the IF spur order. For example, the 2IFx1LO spur is typically 73 dBc for a -10 dBm input with a sine-wave LO, so a -20 dBm IF input creates a spur that is $(2-1) \times (-10 \text{ dB})$ lower, or 83 dBc.

Typical Up-conversion spurious suppression (dBc): I Port (Q Port)

| -10 dBm RF Input | 0xLO | 1xLO | 2xLO | 3xLO | 4xLO | 5xLO |
|------------------|-----------|-----------|-----------|-----------|------|------|
| 0xIF | - | 22 (22) | 59 (59) | 65 (65) | N/A | N/A |
| 1xIF | 31 (31) | Reference | 39 (32) | 13 (13) | N/A | N/A |
| 2xIF | 82 (82) | 73 (67) | 58 (58) | 79 (75) | N/A | N/A |
| 3xIF | 105 (106) | 71 (68) | 87 (76) | 70 (74) | N/A | N/A |
| 4xIF | 113 (113) | 117 (118) | 100 (106) | 126 (121) | N/A | N/A |
| 5xIF | 137 (136) | 118 (119) | 133 (133) | 118 (124) | N/A | N/A |



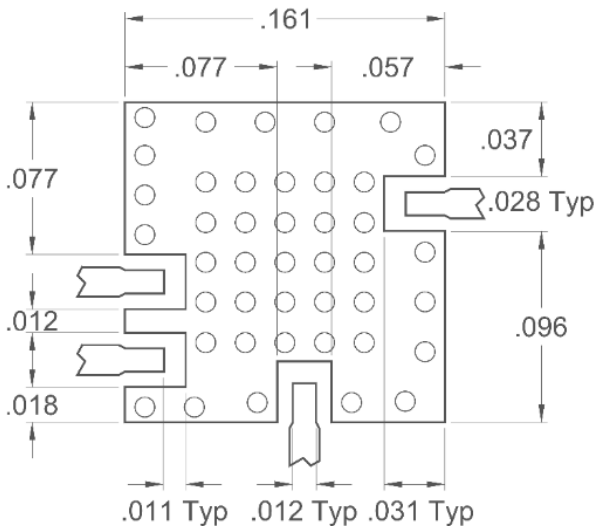
4.1 SM Package Outline Drawing



- Substrate material is ceramic.
- I/O Leads and Ground Paddle plating is (from base to finish):

| | | |
|-----|-------------|------------|
| Ni: | 8.89um MAX | 1.27um MIN |
| Pd: | 0.17um MAX | 0.07um MIN |
| Au: | 0.254um MAX | 0.03um MIN |
- All unconnected pads should be connected to PCB RF ground.

4.2 SM Package Footprint



QFN-Package Surface-Mount Landing Pattern

[Click here for a DXF of the above layout.](#)

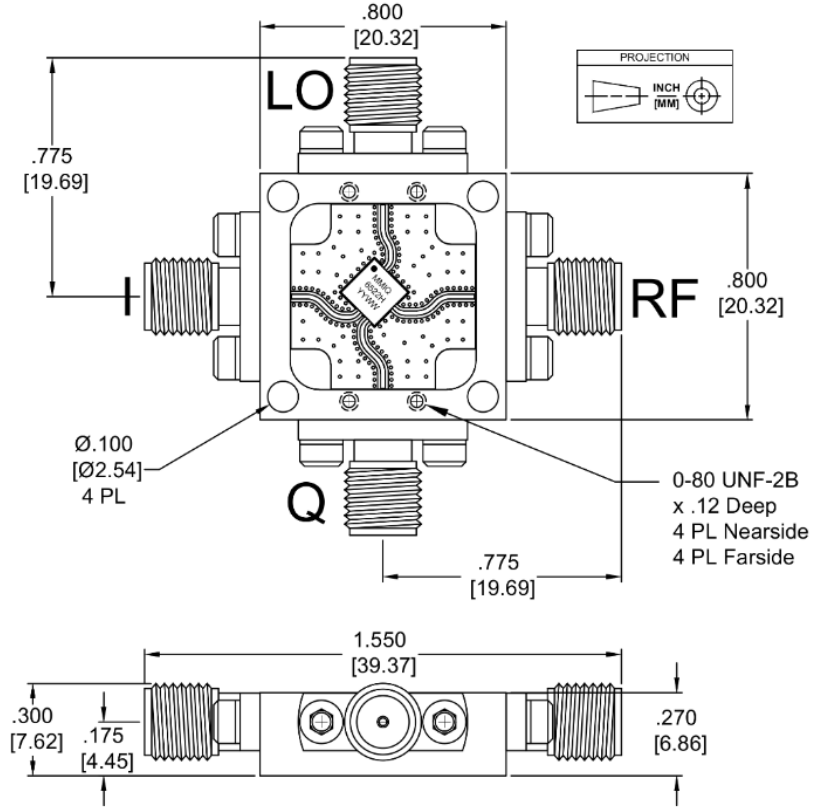
[Click here for leaded solder reflow.](#) [Click here for lead-free solder reflow](#)



4.3 Evaluation Board Outline Drawing

| Port | Connector Type |
|------|----------------|
| LO | SMA Female |
| RF | SMA Female |
| I/Q | SMA Female |

Note: Eval Connectors are not removeable.



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